



PTO/SB/08A (07-05)

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Substitute for form 1449/PFO

## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

*(Use as many sheets as necessary)*

Sheet 1 of 3

**Complete if Known**

Application Number	09/992,120
Filing Date	11/14/2001
First Named Inventor	RONALD HILTON
Art Unit	2128
Examiner Name	SAXENA, AKASH
Attorney Docket Number	AMDH-08157US0 DEL

U. S. PATENT DOCUMENTS

## **FOREIGN PATENT DOCUMENTS**

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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
		Country Code <sup>2</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)			
AAC	6	G.B. No. 2,203,572	10/19/1998	MACGREGOR, R.	FIG 1,4,5,6,7,11,11

**Examiner Signature** *Bleash Raxenel* **Date Considered** *3/24/03*

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Sheet <b>2</b>	of <b>3</b>	Attorney Docket Number	AMDH-08157US0 DEL

<b>NON PATENT LITERATURE DOCUMENTS</b>				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
AK	7	APIKI, S., "Windows on RISC," Byte, April 1994, pp. 109-115.		
	8	BERGH, A., KIELMAN, K., MAGENHEIMER, D., and MILLER, J., "HP3000 Emulation on HP Precision Architecture Computers," Hewlett-Packard Journal, December 1987, pp. 87-89.		
	9	BOYD-MERRITT, R., "IBM, NCR Show Parallel Mainframes," Electronic Engineering Times, November 15, 1993, pp. 16,138.		
	10	DE MAINE, P., and LEONG, S., "Transportation of Programs," Abstract: "Proceedings of the Fifteenth Southeastern Symposium on System Theory, March 1983, pp. 158-164."		
	11	HALFHILL, T., "Emulation: RISC's Secret Weapon," Byte, April 1994, pp. 119-130.		
	12	HALFHILL, T., "80x86 Wars," Byte, June 1994, pp. 74-88.		
	13	MAY, C., "MIMIC: A Fast System/370 Simulator," SIGPLAN '87 Symposium on Interpreters and Interpretive Techniques, June 1987, pp. 1-13.		
	14	POOLE, L., "PowerPC Preview," Macworld, February 1994, pp. 102- 108.		
AK	15	SLATER, M., "Windows NT: Putting the Squeeze on Intel," OEM Magazine, July 1993, pp. 52-57,90.		

Examiner Signature	<i>Akash Saxena</i>	Date Considered	<i>3/24/06</i>
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<i>Re</i>	16	THELEN, R., "The Power Mac's Run-Time Architecture," Byte, April 1994, pp. 131-138.		T <sup>2</sup>
<i>Re</i>	17	THOMPSON, T., and RYAN, R., "Apple, IBM Bring PowerPC to the Desktop," Byte, April 1994, pp. 44-72.		
	18	<del>TIMMINS, L., "Techniques for Translating Object Code into a Reduced Microprocessor Architecture," Proceedings of IESMM International Symposium on Software and Hardware Applications of Microcomputers, February 1986, pp. 181-184.</del>		<i>Can't Read</i>

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